

FIG. 1 (PRIOR ART)

(TEST LOGIC)

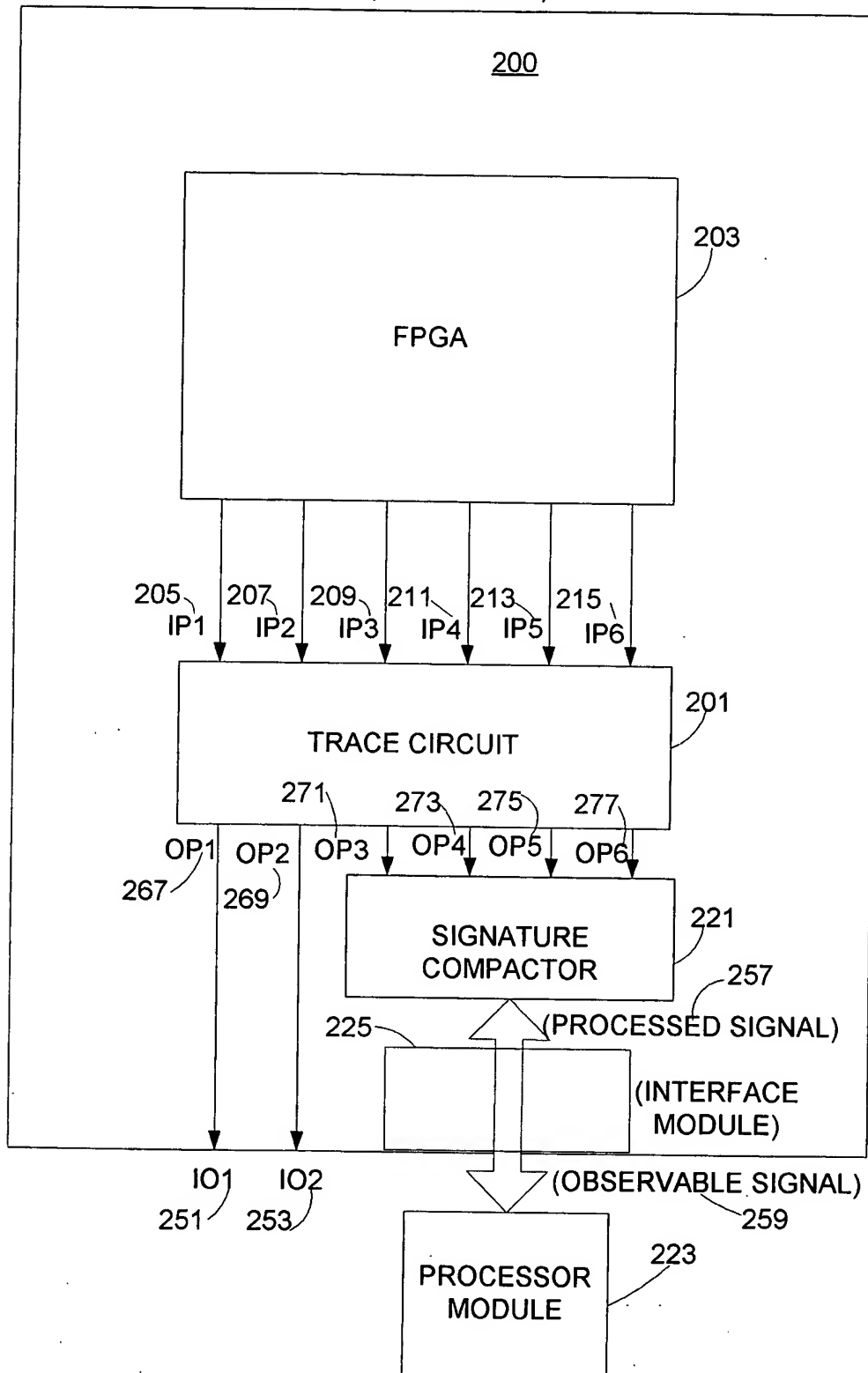


FIG. 2

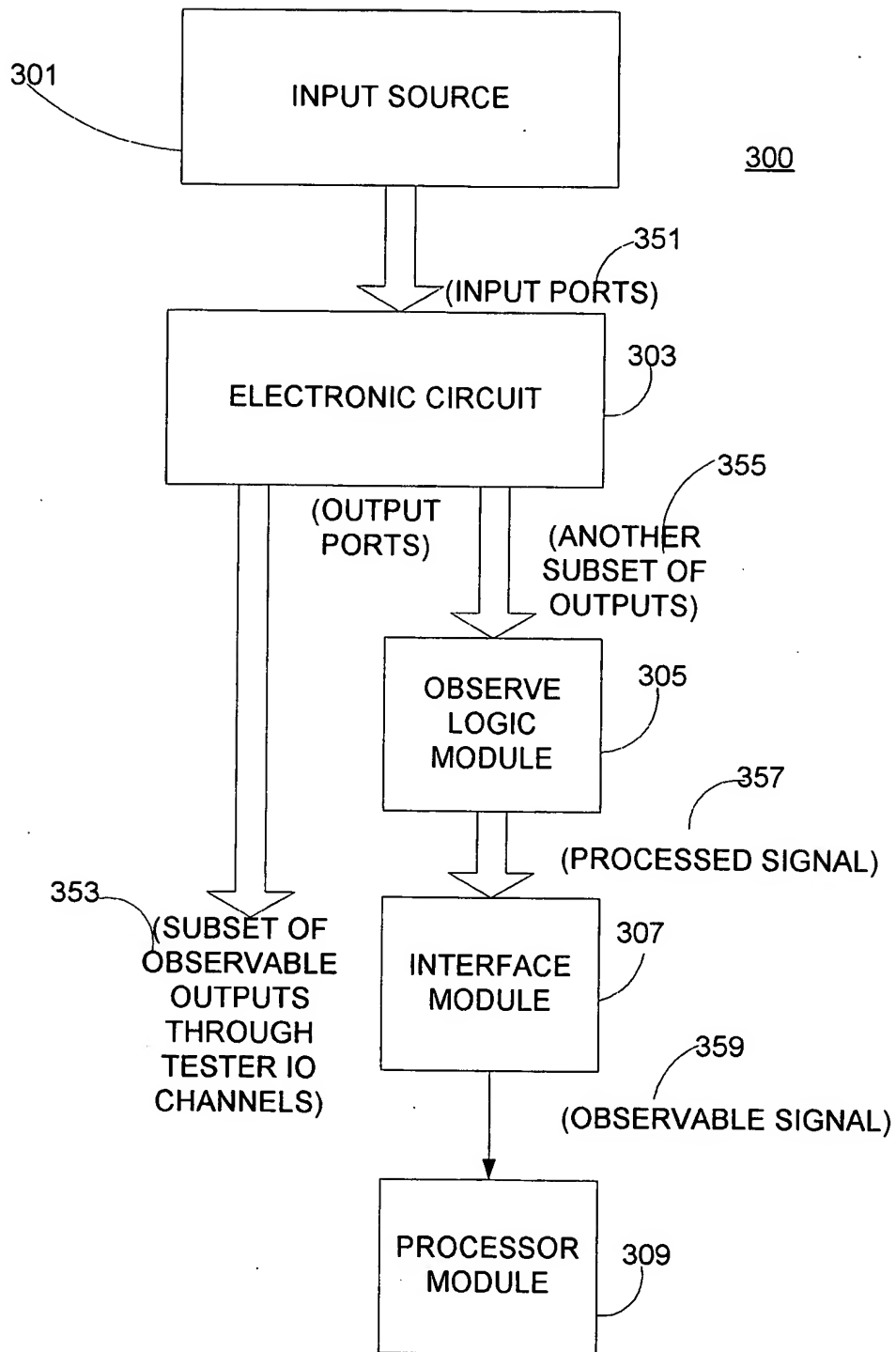


FIG. 3

(TEST LOGIC)

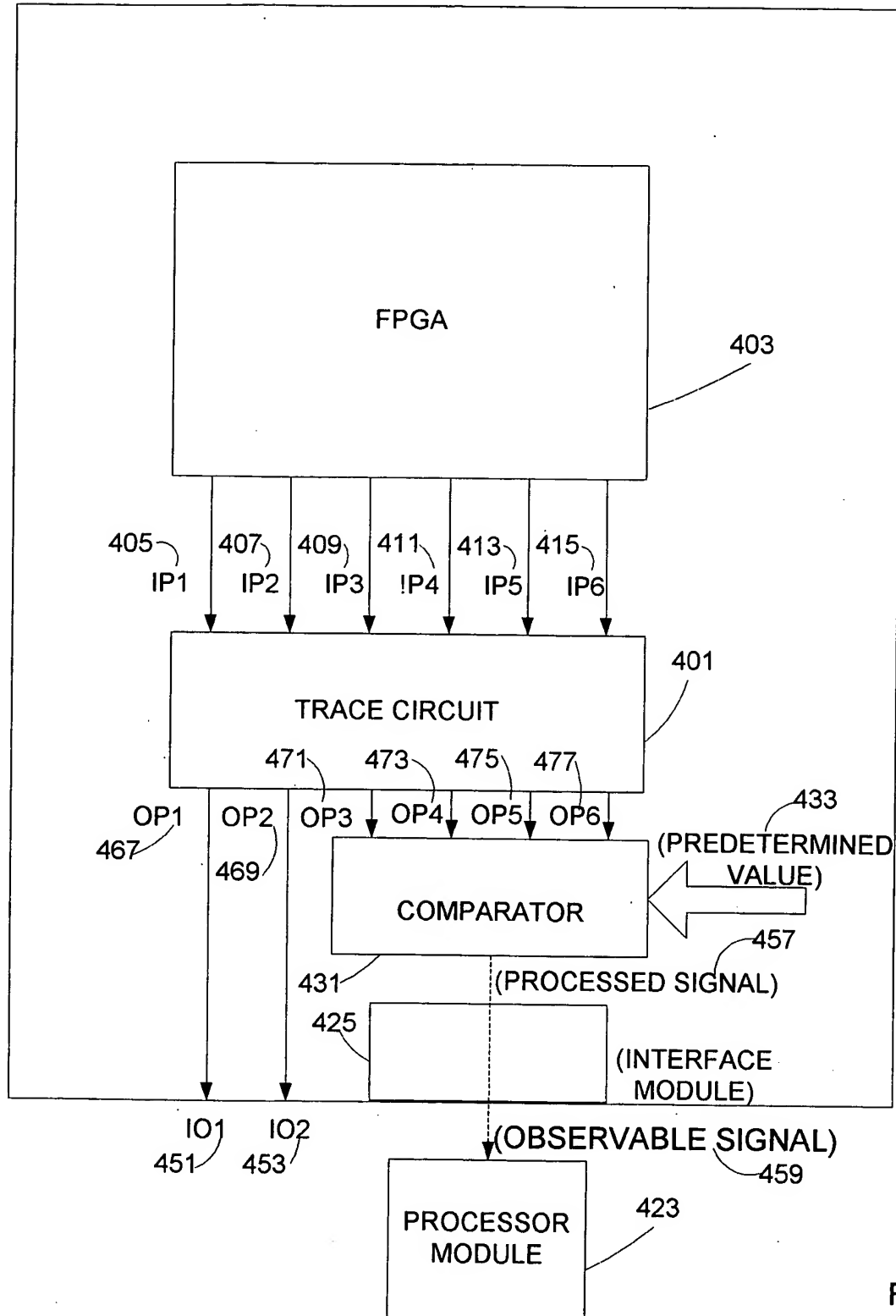


FIG. 4

(TEST LOGIC)

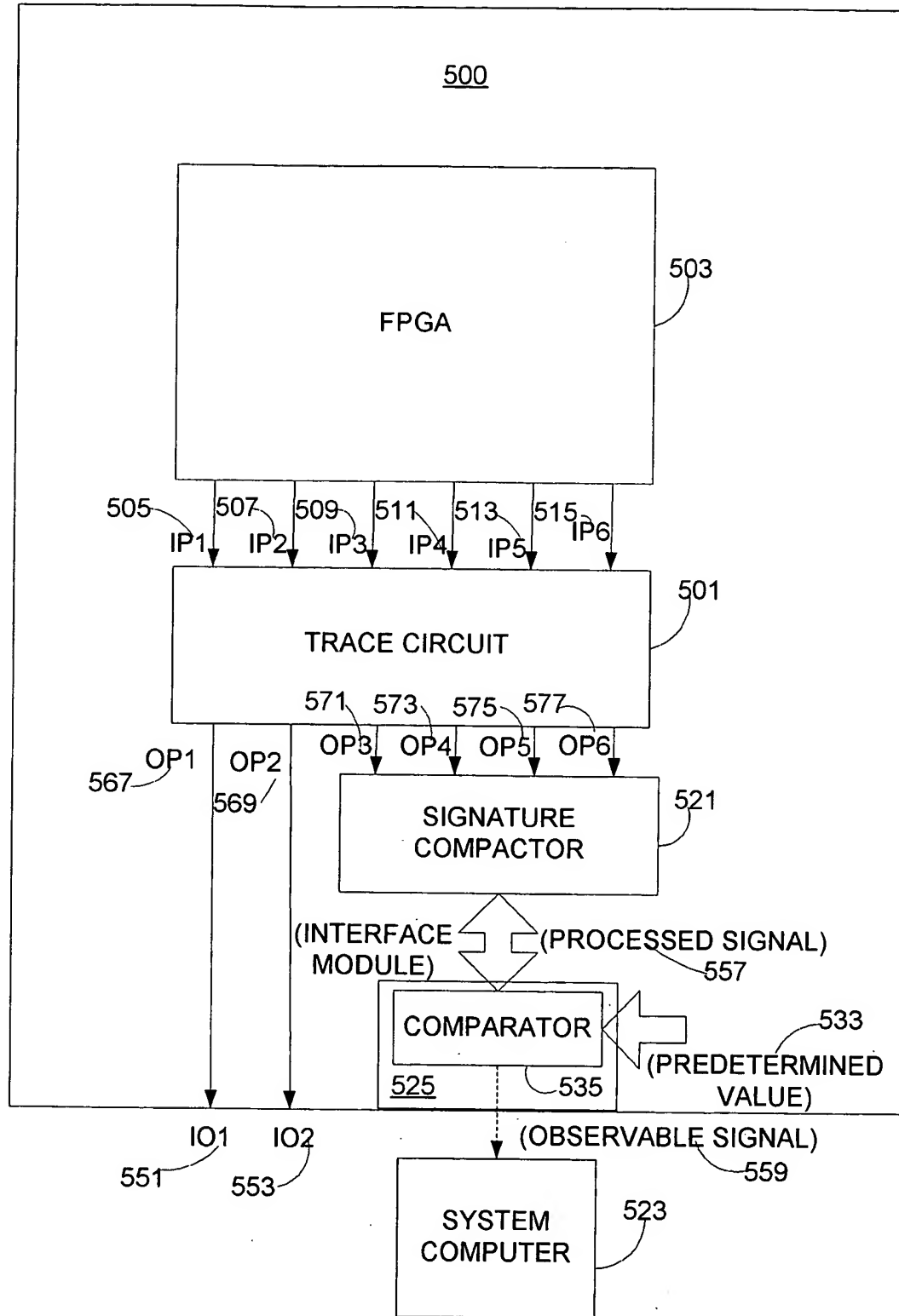


FIG. 5

(TEST LOGIC)

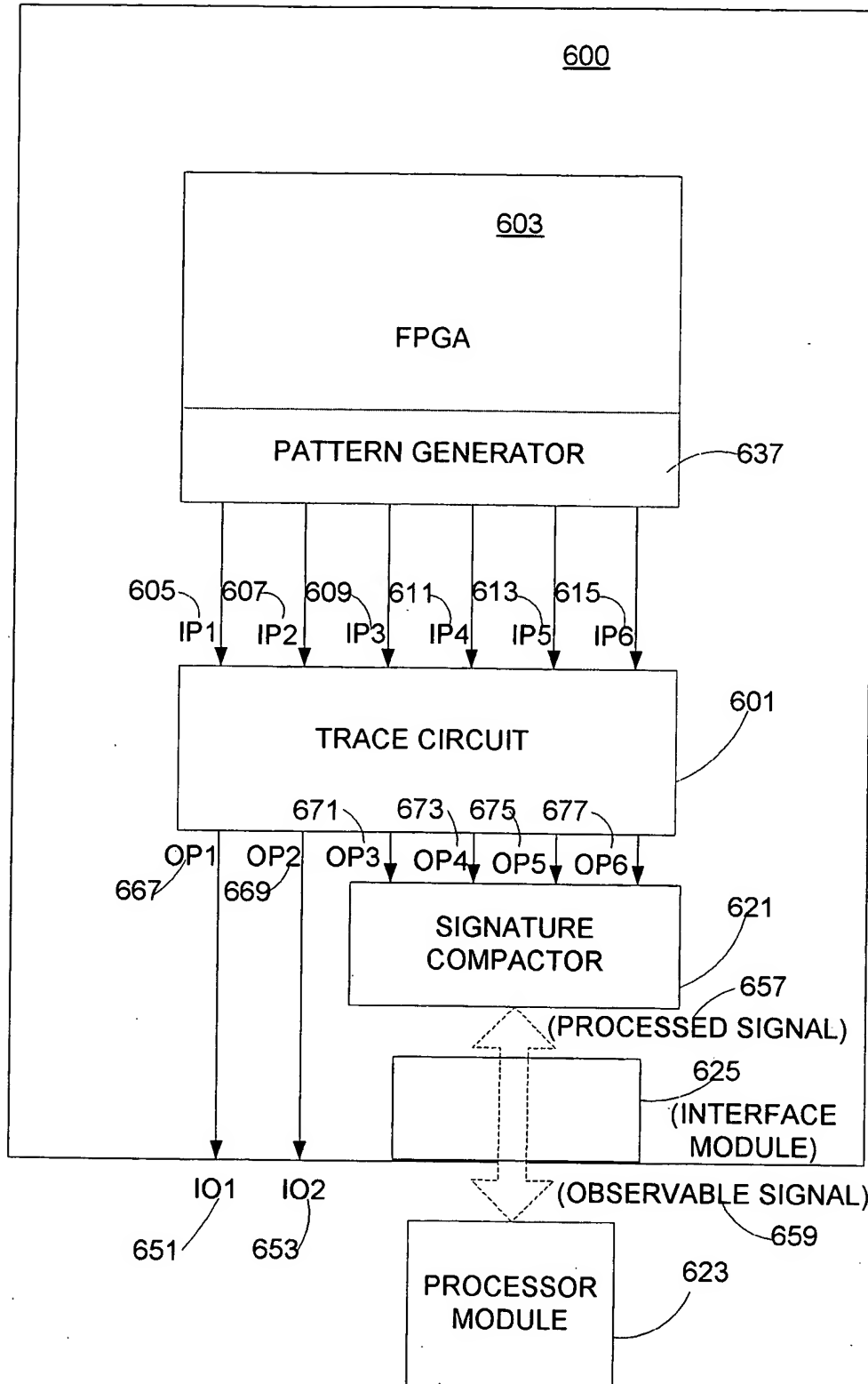


FIG. 6

(TEST LOGIC)

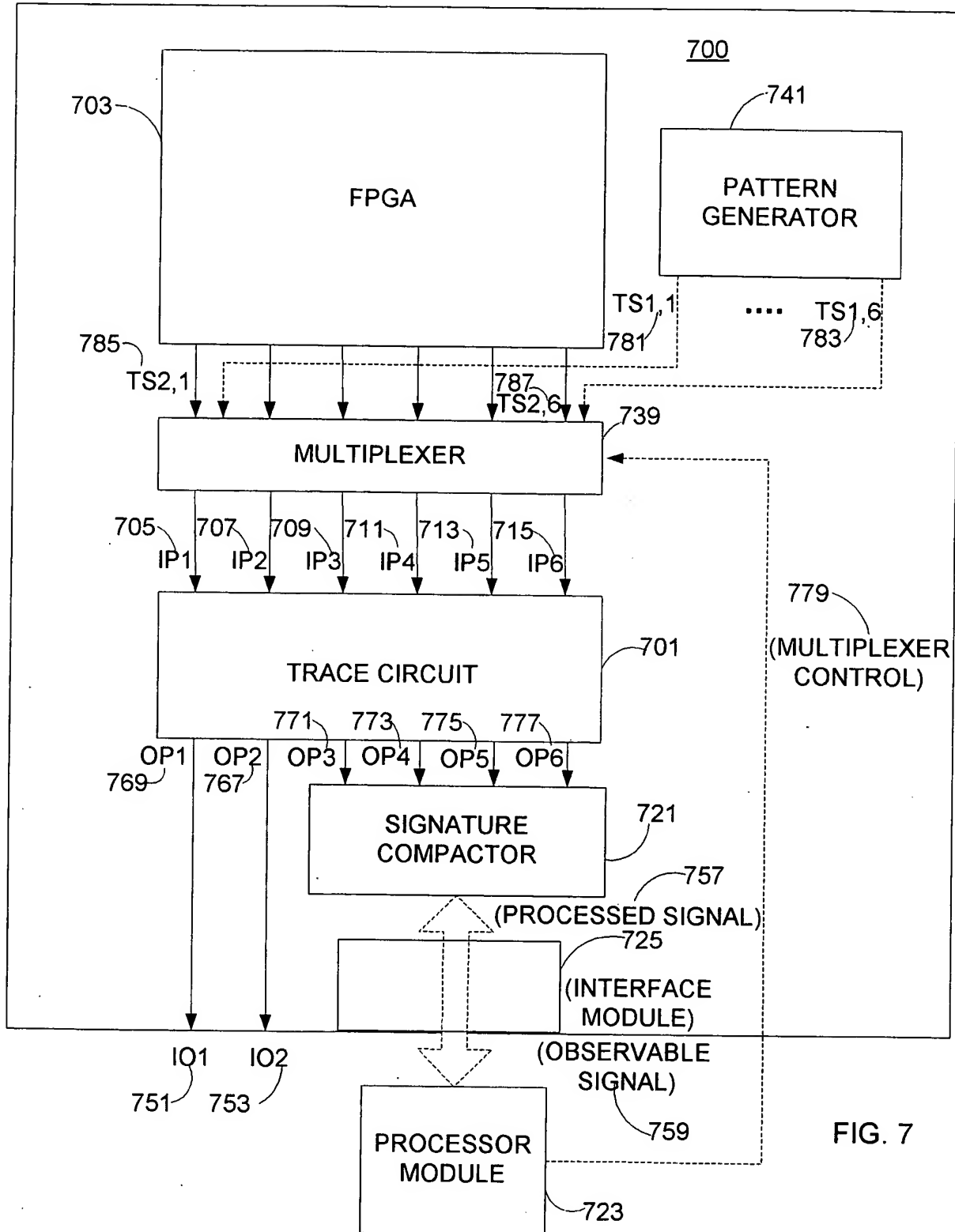


FIG. 7

800

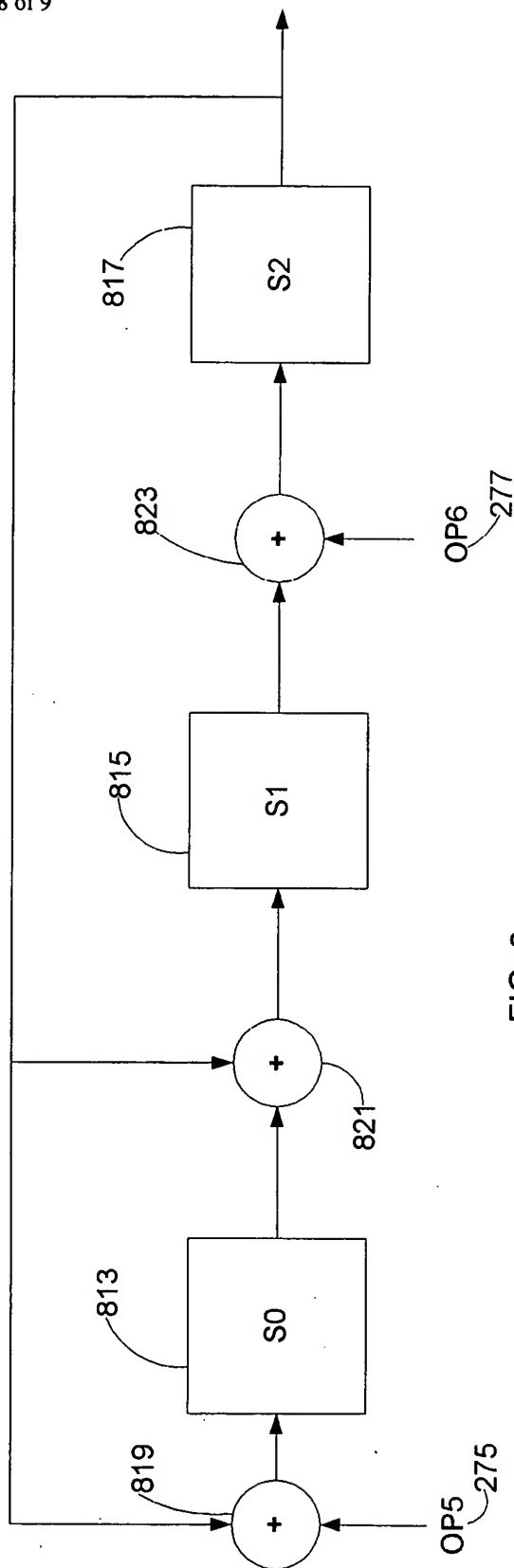
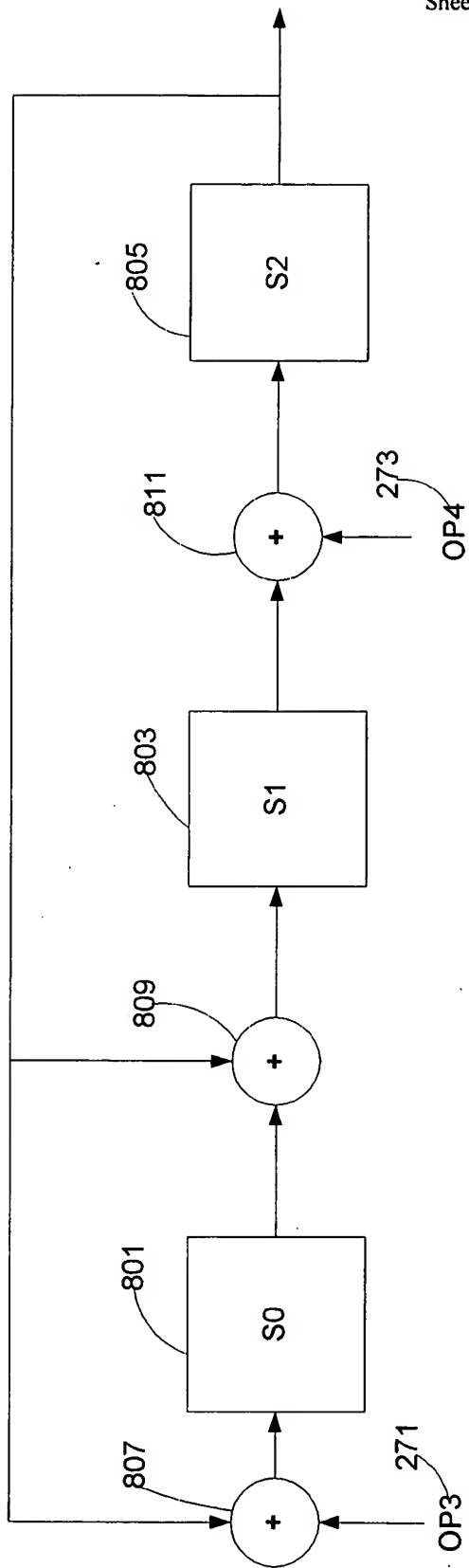


FIG. 8



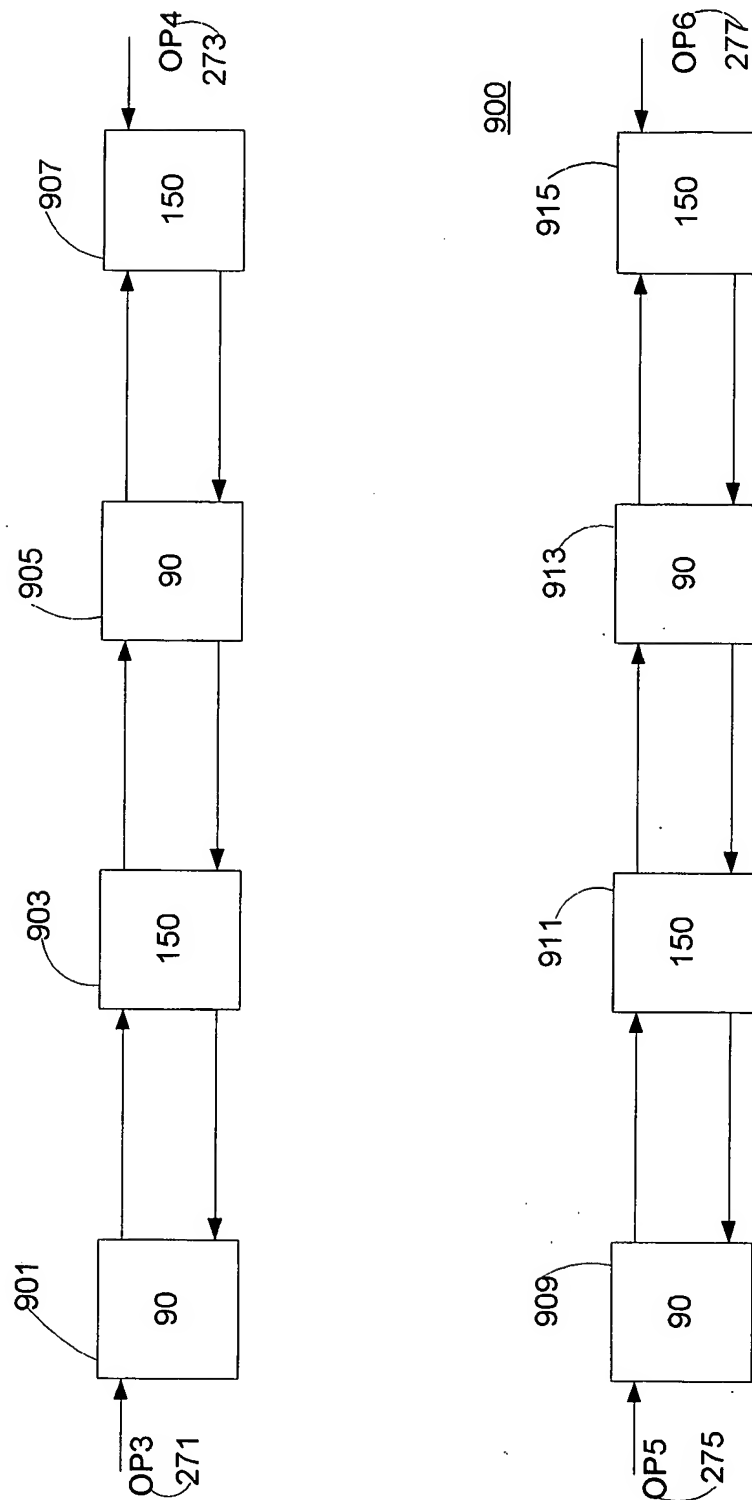


FIG. 9